Reg. No.				

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UG DEGREE END SEMESTER EXAMINATIONS - APRIL 2025.

(For those admitted in June 2023 and later)

PROGRAMME AND BRANCH: B.Sc., ELECTRONICS

SEM	CATEGORY	COMPONENT	COURSE CODE	COURSE TITLE
IV	PART-III	CORE-7	U23EL404	LINEAR INTEGRATED CIRCUITS

Date & Session: 28.04.2025/AN Time: 3 hours Maximum: 75 Marks Outcome Q. SECTION – A $(10 \times 1 = 10 \text{ Marks})$ Course Bloom, Answer ALL Questions. No. CO1 K1 An Op-Amp has _____ open loop voltage gain. 1. a) zero b) infinity c) unit d) negative CO1 K2 2. The common mode rejection ratio of an operational amplifier is given by a) ratio of differential gain to common mode-gain b) ratio of common mode gain to differential gain c) difference between differential gain and common mode-gain d) sum of differential gain and common mode-gain CO2 K1 The Op-Amp integrator and differentiator are useful for_ 3. a) amplification b) sampling signals d) phase shifting c) signal wave shaping CO2 K2 4. The summing amplifier has the___ a) high gain accuracy b) low noise interference c) ability to add multiple input signals d) high input impedance The sample and hold circuit is very useful in_____. CO3 K1 5. b) analog to digital conversion a) digital interfacing c) pulse code modulation d) all of the above Low pass filter pass.... frequencies and stop _____frequencies K2 CO₃ 6. a) low, high b) high, low c) low, low d) high, high CO4 K1 7. A Schmitt trigger converts slowly varying waveform into_____. a) sine wave b) saw tooth wave c) triangular wave d) square wave CO4 K2 The square wave generator is also known as . . 8. a) flip flop oscillator b) free running oscillator c) one shot generator d) filter CO₅ K1 _____ is the application of IC 555. 9. a) power supply regulation b) audio amplification c) timer and oscillator circuits d) all the above CO₅ K2 10. The phase locked loops can be used as a) multiplier b) divider c) AM and FM demodulator d) all the above

Course	Bloom's K-level	Q. No.	$\frac{\text{SECTION} - B \text{ (5 X 5 = 25 Marks)}}{\text{Answer } \frac{\text{ALL}}{\text{Questions choosing either (a) or (b)}}$
CO1	КЗ	11a.	Write the characteristics of an ideal Op-Amp. (OR)
CO1	КЗ	11b.	Define (i) Level translator (ii) Current mirror.
CO2	КЗ	12a.	Write the difference between open loop and closed loop configuration. (OR)
CO2	КЗ	12b.	Predict what would happen with voltage to current converter.
CO3	K4	13a.	Comment on weak Pass Filter and write the applications (OR)
CO3	K4	13b.	Compare band Pass filter and High Pass Filter.
CO4	K4	14a.	Write short notes on comparator. Draw the types of comparator circuits. (OR)
CO4	K4	14b.	Illustrate the operation of level detector.
CO5	K5	15a.	Draw the functional block diagram of IC555 (OR)
CO5	K5	15b.	Define Phase Locked Loops. What are the basic building blocks of PLL?

Course Outcome	Bloom's K-level	Q. No.	<u>SECTION - C (5 X 8 = 40 Marks)</u> Answer <u>ALL Questions choosing either (a) or (b)</u>
CO1	КЗ	16a.	Explain the block diagram of op-amp (IC 741) (OR)
CO1	КЗ	16b.	Explain the purpose of differential amplifier with basic circuit diagram
CO2	K4	17a.	Draw the circuit diagram and explain Inverting and non-inverting amplifier (OR)
CO2	K4	17b.	Compare Integrator and Differentiator and give one application.
CO3	K4	18a.	Explain second order high pass filter in detail with circuit diagram. (OR)
CO3	K4	18b.	Elaborate Sample and Hold Circuit using Op-Amp. Write the applications
CO4	K5	19a.	Explain Wien Bridge Oscillator using Op-Amp with circuit diagram
CO4	K5	19b.	(OR) Paraphrase Triangle Wave generator using Op-Amp.
CO5	K5	20a.	Explain Voltage Controlled Oscillator using IC566 (OR)
CO5	K5	20b.	Choose a Multivibrator using IC555 and explain its operation.